

| | Document ID | Issue Date | Pages | Title | Current OR | Current XRef | Inventor |
|---|--------------|------------|-------|---|------------|--|---------------------------------------|
| 1 | US 5177567 A | 19930105 | 11 | Thin film structure for chalcogenide electrical switching devices and process therefor | 257/4 | 257/3 ; 257/760 | Klery, Patrick J. , et al. |
| 2 | US 5166096 A | 19921124 | 12 | Process for fabricating self-aligned contact studs for semiconductor structures | 438/586 | 257/752 ; 257/900 ; 438/595 ; 438/596 ; 438/672 | Cote, Denna K. , et al. |
| 3 | US 4765657 A | 19890103 | 17 | Method of fabricating a programmable array | 427/96 | 365/100 ; 365/163 | Formigoni, Napoleon P. , et al. |
| 4 | US 4630355 A | 19861223 | 18 | Electric circuits having repairable circuit lines and method of making the same | 438/6 | 148/DIG.55 ; 257/204 ; 257/209 ; 257/537 ; 29/593 ; 29/829 ; 438/131 | Johnson, Robert R. |
| 5 | US 5831276 A | 19981103 | 28 | Three-dimensional container diode for use with multi-state material in a non-volatile memory cell | 257/3 | 257/4 ; 257/5 ; 257/594 | Gonzalez, Fernando , et al. |